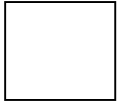


**Lab 8 - PCB Design: FUSION Software - 15 Marks - 2% of Final Mark**

Student Name: \_\_\_\_\_

Student ID Number: \_\_\_\_\_ Date: \_\_\_\_\_

**Lab Rules:**

- Safety glasses are mandatory in the lab when the power is ON.
- No food or drink is allowed in the lab.
- Follow all [SEMET Lab Rules](https://tinyurl.com/SEMET-home) (<https://tinyurl.com/SEMET-home>)
- Lab must be handed in by the end of the lab.

**Lab Objectives:**

1. Create a new printed circuit board (PCB) project using FUSION software.
2. Identify and select recommended parts in accordance with functional specifications.
3. Design a schematic that conforms to the Electrical Rules Check and correct any errors.
4. Design a PCB layout that conforms to the Design Rules Check and correct any errors.
5. Create and manage board dimensions, trace widths, silkscreen text, and overall design.
6. Create a set of manufacturing files (Gerbers) and upload them to MySeneca.

**Lab Materials:**

1. Lab computer for MySeneca and Autodesk FUSION software.

**Part 1: Introduction and Background**

All modern electronics rely upon Printed Circuit boards (PCBs) to provide a compact, efficient, and reliable connection between various components.

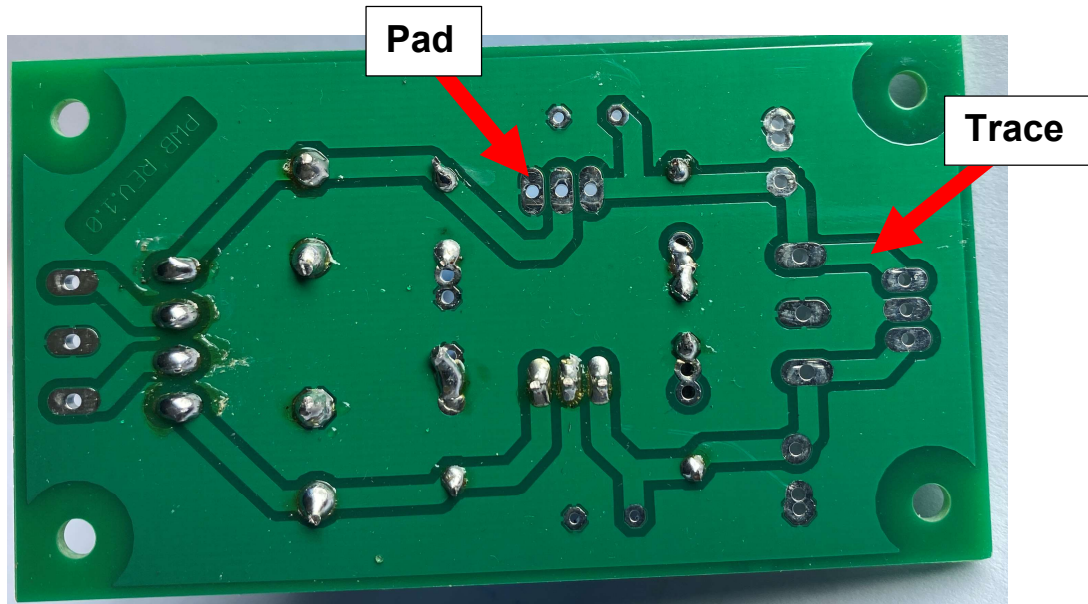
PCBs are usually comprised of one or more layers of fiberglass and copper laminated together, where the copper is designed in specific patterns. Circuit boards serve two main purposes:

- Provide a stable and rigid surface to mount components
- Create electrical connections between the components

To achieve these objectives, several features are etched into the copper of a PCB:

**Pads** - Pads are the locations in which components are inserted into the board and soldered.

**Traces/Tracks** - Traces are the metal paths used to connect various components together.



*Figure 1 - Soldered side of a printed circuit board (PCB), noting pads and traces.*

From **Figure 1** above, students can practice doing the following:

- Count the number of pads and traces present,
- Identify the location and number of mounting holes, and
- Locate the revision board number.

Note: Students can design and print PCBs for free using SEMET resources. This lab paper will cover all the steps to create manufacturing files which can be sent to the PCB lab for printing.

Keep this lab as a handy guide as you will be required to design PCBs for upcoming classes!

**If you are using this reference as a guide:** Pay particular attention to the instructions in Step 7, which outlines 3 cases to make your design conform to the restrictions required by the PCB Lab.

A **SEMET.Ibr** file is now available to students that supports the first two semesters in SEMET.

## Part 2: Schematic Capture 6 Marks

Our first step in PCB design is to create a **Schematic** based upon the parts that will be used in the board layout. This task will cover the process of creating a project and Schematic Capture.

1. Launch Autodesk FUSION 360 from MyApps. Login to your Autodesk account.
2. On the **Data Panel**, click **New Project**.

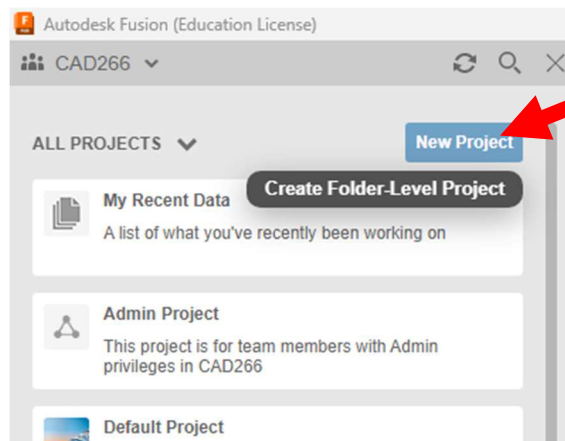


Figure 2 - Create a new project.

Name the project: **LIN155NxxL\_LAB8\_Lastname\_Firstname**

Put your first and last name and lab section as part of the project name.

3. Navigate to the **Menu bar**, select **File > New Electronics Design**.

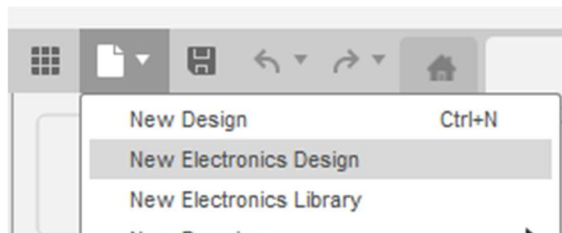


Figure 3 - Create a new electronics design.

4. Navigate to the **COMMON** tool bar and select **New Schematic**.

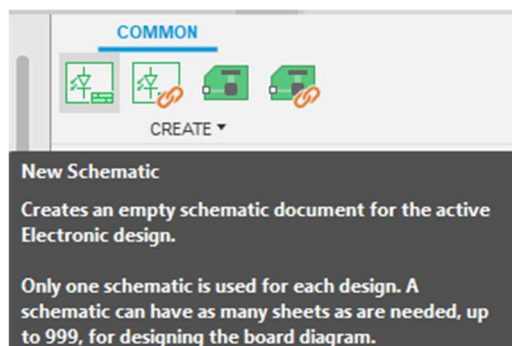


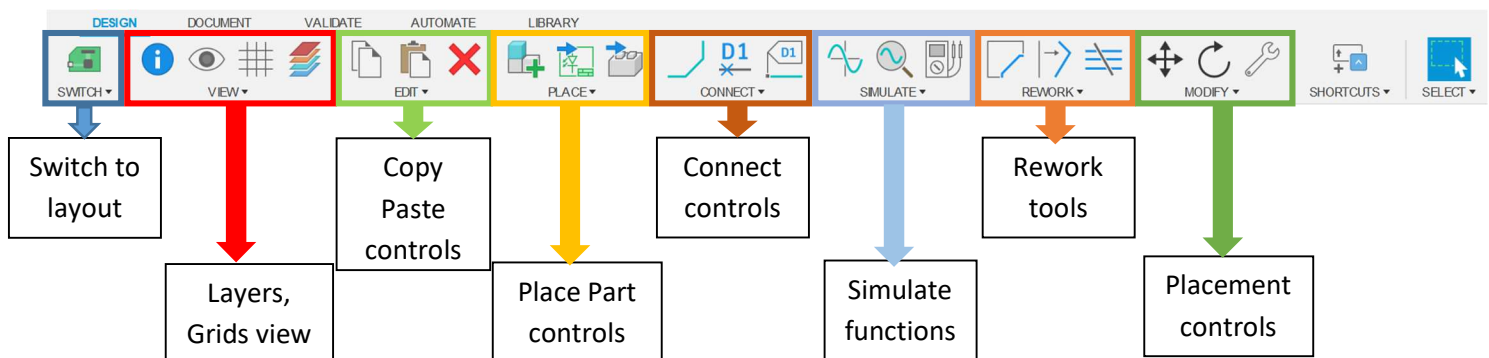
Figure 4 - Create a new schematic.

- Click **Save** and name the schematic **LIN155\_Lab8**.

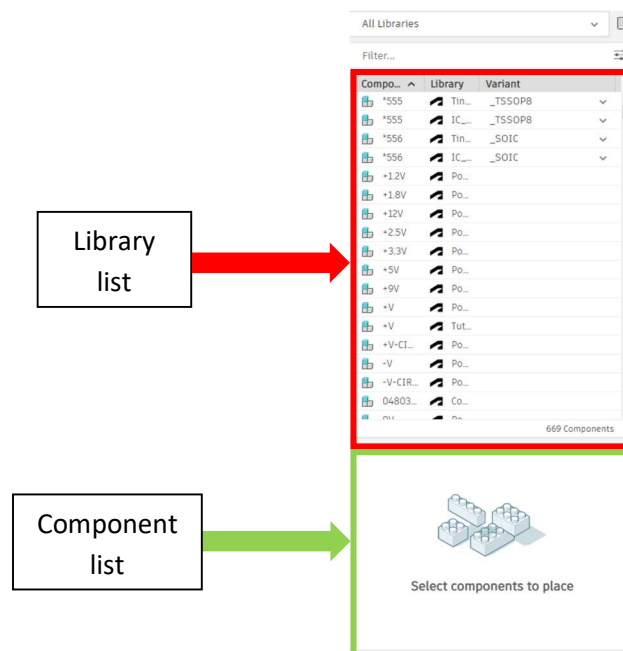


**Figure 5 - Save the project name.**

- The schematic editor will open. Get familiar with locations of the main controls and their uses.



**Figure 6 - Fusion schematic tool bar.**

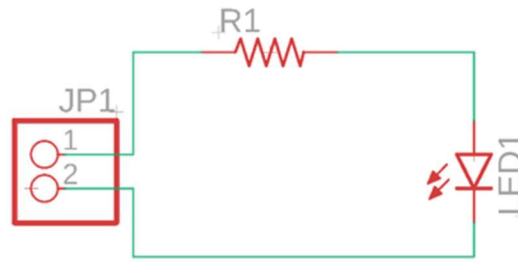


**Figure 7 - Library manager.**

We will follow each of these steps to complete the Schematic Capture design:

- Add Parts** to the design by searching and selecting each one from the library,
- Connect** the parts using the Net tool, to indicate where the traces will go,
- Run the ERC** or Electrical Rules Check to check for errors and fix them.

7. Let draw the schematic in **Figure 8** below.



**Figure 8** - LED circuit.

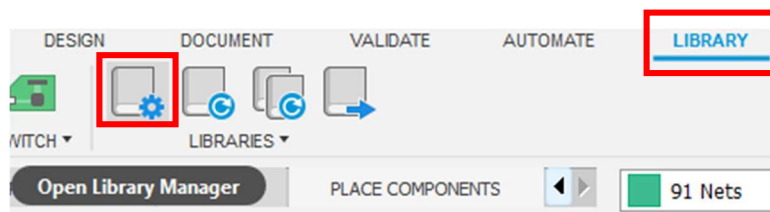
The schematic consists of:

- one **Resistor**: 1 k $\Omega$ , 1/4 W
- one **LED**: 5 mm
- one **Connector**: two-position, 2.54 mm/0.1" pitch (also called a header pin)

8. Obtain the **SEMET.lbr** library from LIN155.CORE (SEMET site) and save to local computer.

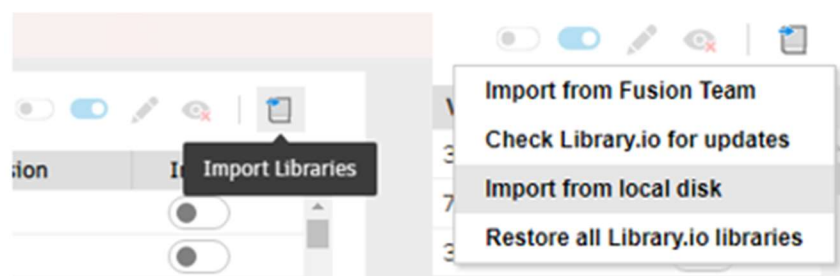
9. Add SEMET.lbr library to FUSION.

- Click on menu **LIBRARY** then select **Open Library Manager**.



**Figure 9** - Opening Library Manager.

- Once the **Library Manager** open, click on **Import Library**. Then select **Import from local disk**.



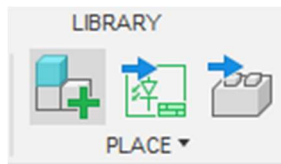
**Figure 10** - Import libraries from local disk.

- Select **SEMET.lbr** then click **Open**.
- Once the library open, a list of components should be seen as the figure below.

Component ^	Library	Variant
555	SEMET	DIP
7400	SEMET	N
7402	SEMET	N
7404	SEMET	N
7408	SEMET	N
7432	SEMET	N
7486	SEMET	N
74LS47	SEMET	N
78XX	SEMET	
7SEG-CA	SEMET	
7SEG-CK	SEMET	
BJT_NPN	SEMET	2N3904
BJT_PNP	SEMET	2N3906
CAP-CER	SEMET	CAP-2....
CAP-POL	SEMET	E2,5-6E
DB9CON	SEMET	"
DIODE	SEMET	DO35-7
DIODE	SEMET	

**Figure 11** - List of available components.

10. Begin adding parts using the **Place component** button, found under the **PLACE** section.



**Figure 12** - Place component button.

11. To add a resistor:

- In the Filter... box, type **res**.

res	×	≡
Component ^	Library	Variant
74LS47	SEMET	N
RESISTOR	SEMET	AXIAL-7... ▾

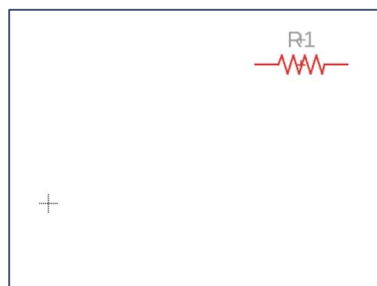
**Figure 13** – Searching for a resistor.

- Click on  symbol of the **RESISTOR** line and choose the **11.7mm** variant.



**Figure 14** - Select 11.7 mm resistor variant.

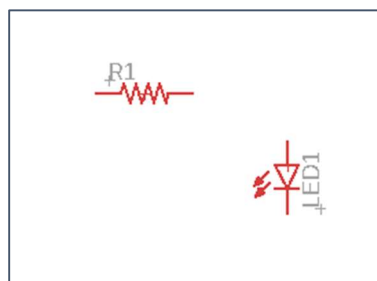
- Double-click to select then drag place the resistor on the workspace.



**Figure 15** - Placing a resistor to the schematic diagram.

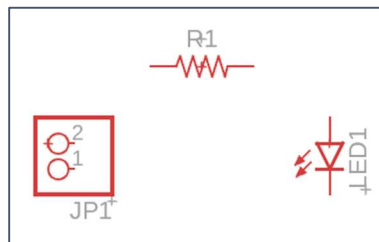
**12.** Next, search for **LED** and place a **LED\_5MM**.

You can rotate the part by right-clicking before placement.



**Figure 16** - Schematic showing a resistor and a LED, unconnected.

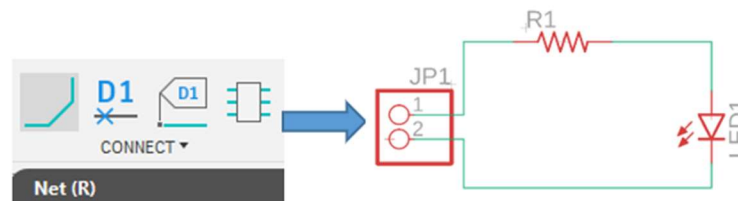
13. Now add a 2-position pin header to the schematic: search for **PIN** then add a **PINHD-1X2**



**Figure 17** - 2-position pin header is added to the schematic.

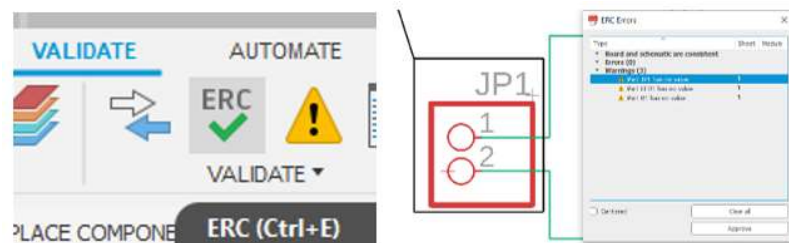
**Notice** that the pins of JP1 are facing LED1. Either **rotate** or **mirror** JP1.

14. Using the **Net** tool, connect the circuit parts together as shown in **Figure 18** below.



**Figure 18** - Select Net tool to connect the parts.

15. Click on menu **VALIDATE** then click on the **ERC** button.



**Figure 19** - Click "ERC" to run the **Electrical Rules Check**.

16. The ERC Errors window will pop up and **highlight** any **errors** or **warnings**, such as unconnected pins, accidental connections between power and ground, missing values, and others.

**Review all errors** and **warnings**. **Fix** them if necessary. For example, for any missing values, select the part and right-click it again to select its Value in the menu, then fill it in.

If no correction is needed on the error, click on "Approve" to remove it from the list.

When the schematic is complete and error-free, we can proceed with the board layout design.

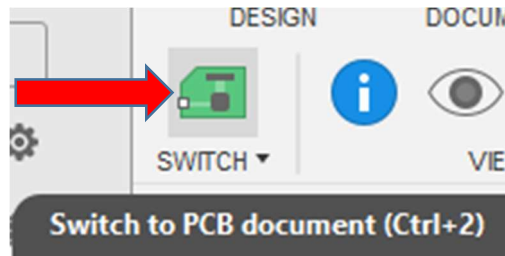
Call your instructor for a signature: \_\_\_\_\_ ( 1 2 3 4 5 6 Marks)



## Part 3: PCB Layout 6 Marks

Our second step in PCB design is to convert the schematic information into a physical board layout. This task will cover the process of the PCB Layout.

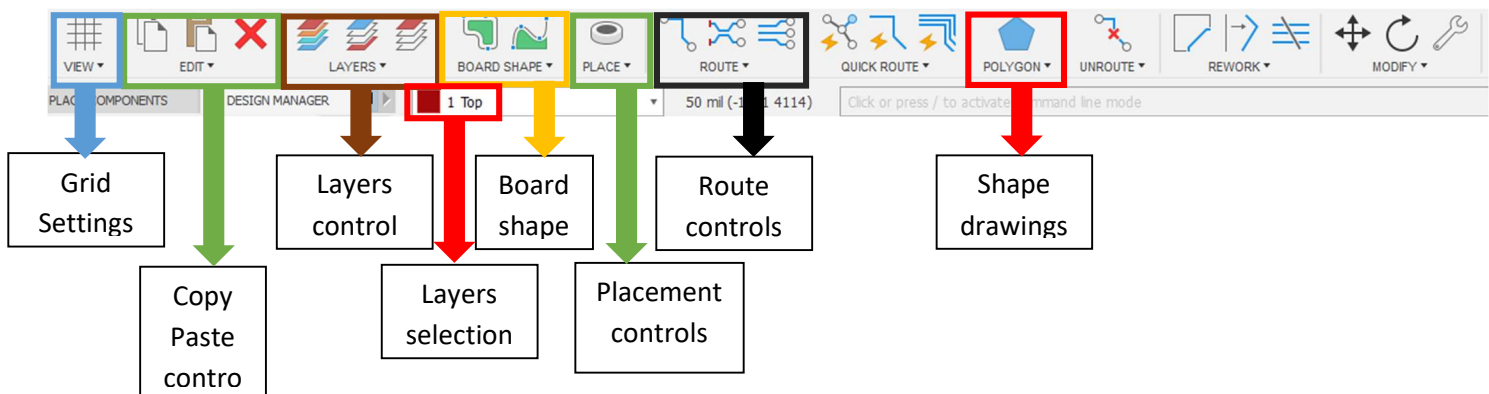
1. Click the **Switch to PCB document** button to switch to the board editor.



**Figure 20** - Find the **Switch to PCB document** button at the top of the screen.

2. The board editor should open. Get familiar with locations of the main controls and their uses.

**Figure 21** - Board Editor window, side panel toolbar.



We will follow each of these steps to complete the PCB Layout design:

- a) Set **design rules**.
- b) Set **board outline** dimensions.
- c) **Place the parts** within the board dimensions in their desired locations,
- d) Replace all schematic Nets with physical copper **Traces** on the board,
- e) Add **text** to the silkscreen layer to indicate name, revision, etc.

Set Design Rules

3. Click on menu **RULES** and select **Design Rules**.



*Figure 22 - Select Design Rules tool.*

4. Set the signal clearance to 14 mil as **Figure 23** below.

Wire - Wire Clearance	14 mil
Wire - Pad Clearance	14 mil
Wire - Via Clearance	14 mil
Pad - Pad Clearance	14 mil
Pad - Via Clearance	14 mil
Via - Via Clearance	14 mil
SMD - Pad Clearance	14 mil
SMD - Via Clearance	14 mil
SMD - SMD Clearance	14 mil

*Figure 23 - Clearance rules.*

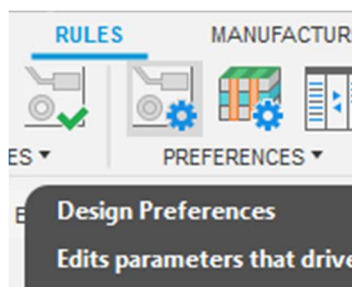
5. Set the **Minimum Width** and **Minimum Drill** as below. Then click **OK**.

Copper Width	18 mil
Drill Size	1mm

*Figure 24 - Sizes rules.*

Note: For Drill Size, enter "1 mm". It will automatically convert to 39.37 mil.

6. Navigate to menu **RULES**, then select **Design Preferences**.



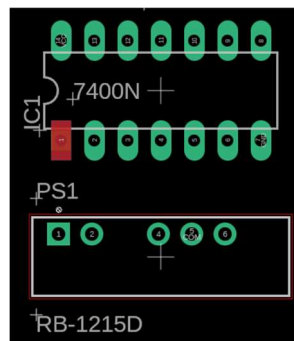
*Figure 25 – Design Preferences.*

7. Select tab **Annular Ring**. **IMPORTANT!** This step is essential in getting PCBs printed by the SEMET PCB lab. Please follow the instructions carefully. If you are:
- First semester** students and doing LIN155 Lab 8, follow **Case 1**.
  - Second** or **higher semester** students and using this document as PCB guideline, choose one of the following 3 cases accordingly. Your Fusion project is:
- **Case 1:** Using **ONLY** SEMET.lbr
    - Verify the default values as Table 1 below.

	Min	%	Max
Top	10 mil	25	20 mil
Inner	10 mil	25	20 mil
Bottom	10 mil	25	20 mil

**Table 1** - Annular Ring default values.

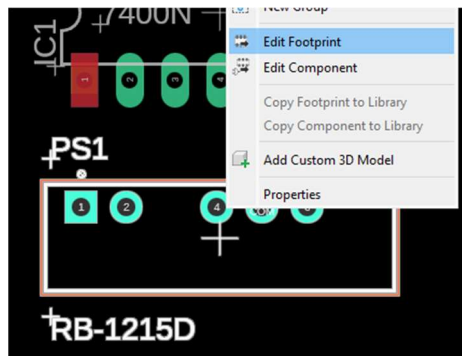
- Skip Case 2 and Case 3. Move on to Step 8.
- **Case 2:** Using other libraries along with SEMET.lbr. For example: Figure 26 below shows 7400N part from SEMET.lbr and the RB-1215D from another library.



**Figure 26** - Example of using SEMET.lbr with additional libraries.

- Verify the Annular Ring value is set as Table 1 above.

- ii. Right click on the RB-1215D then select **Edit Footprint**. Click **YES** when prompted.



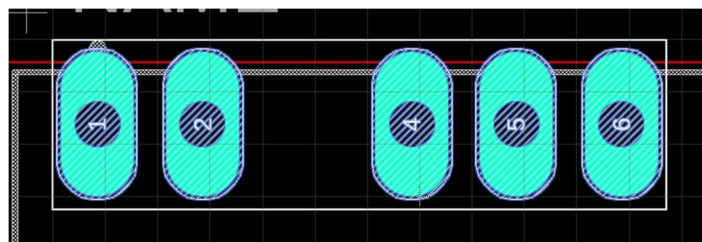
*Figure 27 - Select Edit Footprint.*

- iii. Select all the pads then open the **INSPECTOR** window.
- iv. Change the **Shape** to **long** and **Angle** to **90** degrees.



*Figure 28 - Change the angle and shape of the pads.*

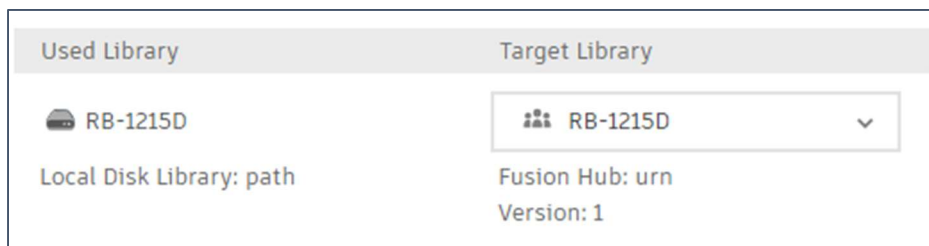
- v. All the pads should change to the shape below.



*Figure 29 - Pads shape and angle changed.*

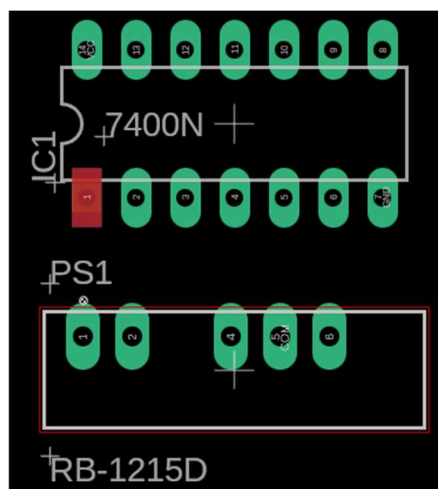
- vi. Verify the **Drill** size and change if necessary.
  1. If the size is **smaller** than **39.37** mils, **change it to 39.37** mil. Also change the **Diameter** to **70** mil.
  2. If the size **greater than or equal** to **39.37** mils, **leave the Drill** and **Diameter** as **default**.
- vii. Save the library. This step will save a copy of this library to Fusion Hub.

- viii. Right click again on the RB-1215D then select **Swap Library**.
- ix. Verify the Used Library is the local disk version, and the Target Library is the Fusion Hub version. Then click SWAP.



**Figure 30** - Verify the used and target library before swap.

- x. Verify RB-1215D on the PCB has swapped to the new version.



**Figure 31** - Library swapped.

- xi. Repeat from step ii to ix for other components if needed. Then move on to step 8.
- **Case 3:** NOT using SEMET.lbr and only using other libraries.
    - i. Set the Annular Ring as Table 2 below.

	Min	%	Max
Top	15 mil	40	40 mil
Inner	10 mil	40	40 mil
Bottom	15 mil	40	40 mil

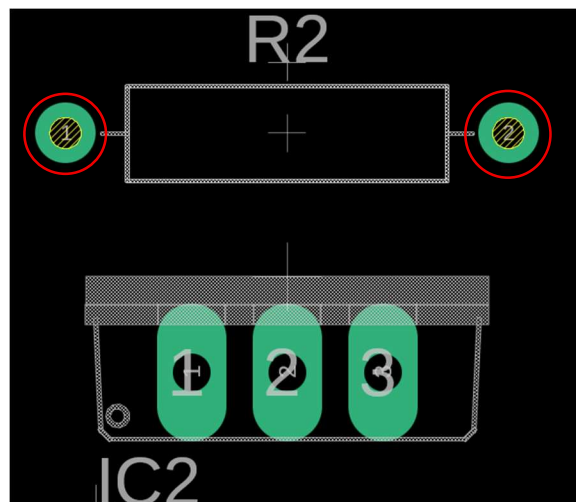
**Table 2** - Annular Ring values when NOT using SEMET.lbr

- ii. Check the Diameter box.



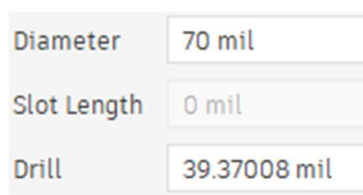
**Figure 32** - Check the Diameter box.

- iii. Verify all the drill holes are at least 39.37 mils (or 1 mm). The ones that smaller than 39.37 mils are highlighted as shown in the figure below.
  - R2 drill holes are smaller than 39.37 mils => highlighted by yellow lines
  - IC2 drill holes are greater than or equal to 39.37 mils.



**Figure 33** - Under sized drill holes are highlighted.

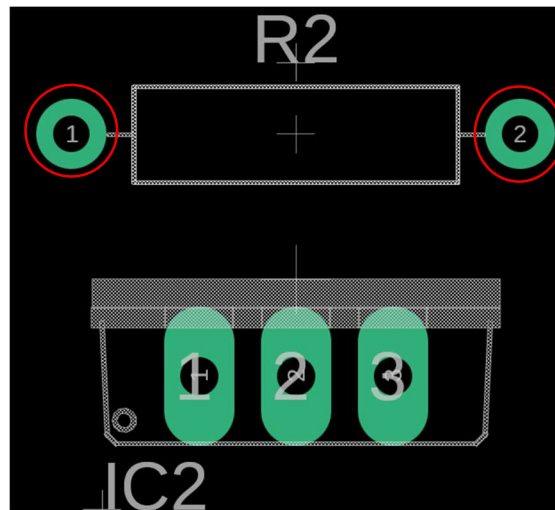
- iv. Right click on the component with the highlighted drill holes then select **Edit Footprint**. Click **YES** when prompted.
- v. Select all the pads then open the INSPECTOR window.
- vi. Change the Drills size and Diameter as below.



**Figure 34** - Set Diameter and Drill size for pads.

- vii. Save the library. This step will save a copy of the original library to Fusion Hub.
- viii. Navigate back to the PCB. Right click on the same component again then select Swap Library.
- ix. Verify the Used Library is the Library.io version, and the Target Library is the Fusion Hub version. Then click SWAP.

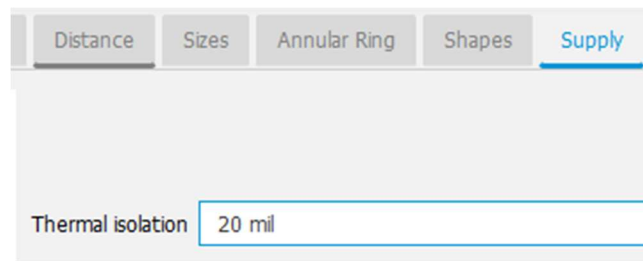
- x. Verify the component has been swapped (yellow line highlight disappeared).



*Figure 35 - Component swapped.*

- xi. Repeat from step iv to ix for all other components if needed. Then move on to step 8.

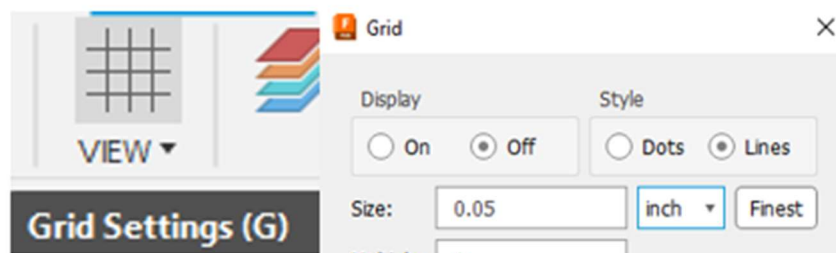
8. Click on tab **Supply** and set the **Thermal isolation** to **20 mil**. Then click **OK**.



*Figure 36 - Supply rules.*

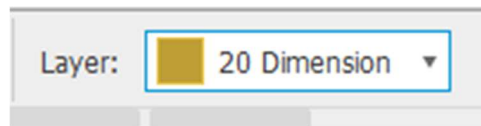
### Board Outline

9. Click on **Grid Settings** then change the unit to **inch**. Click **OK**.



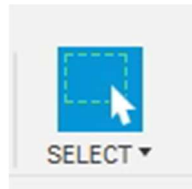
*Figure 37 - Change grid unit to inch.*

10. Set the active layer to the dimensions layer, as shown in **Figure 28**.



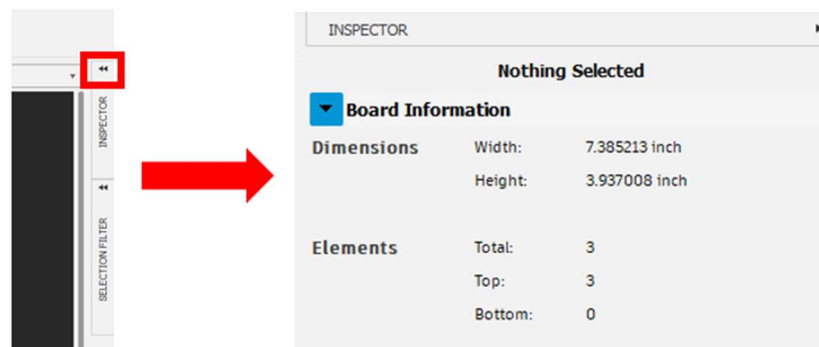
**Figure 28** - Dimensions layer selected.

11. Select the **SELECT** tool.



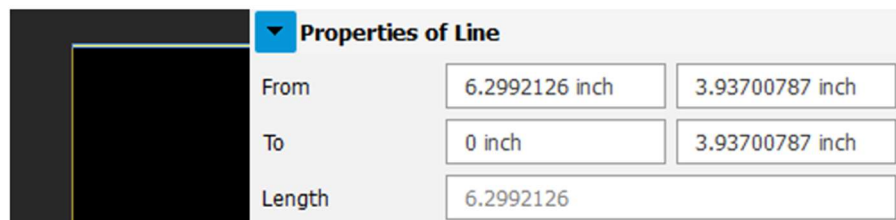
**Figure 39** - SELECT tool.

12. Open the **INSPECTOR** windows.



**Figure 40** - Opening the inspector windows.

13. Click on the top edge of the board outline.



**Figure 41** - Top edges coordinates.

14. Change the coordinate of this line as **Figure 32** below.



**Figure 42** - Set new coordinates.



15. Select the bottom edge of the board outline.



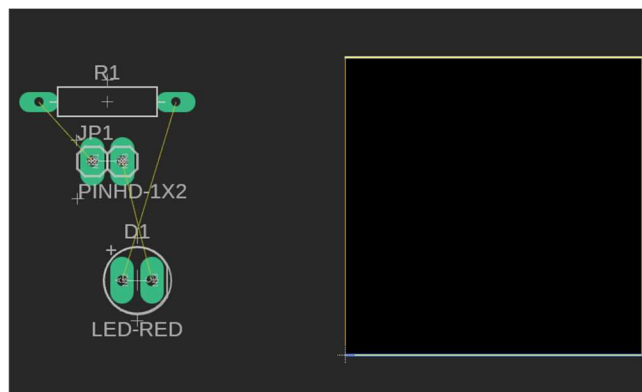
**Figure 43** - Bottom edges coordinates

16. Change the coordinate of this line as **Figure 34** below.



**Figure 44** - Set new coordinates for the bottom edge.

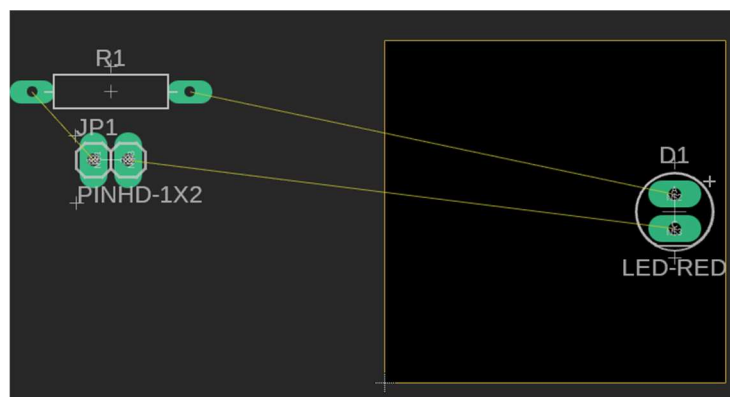
17. The board outline size now set to 1" x 1".



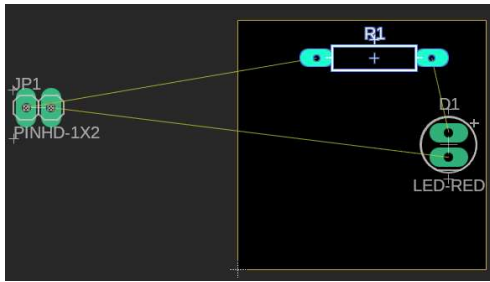
**Figure 45** - Resize board outline to 1" x 1".

### Component Placement

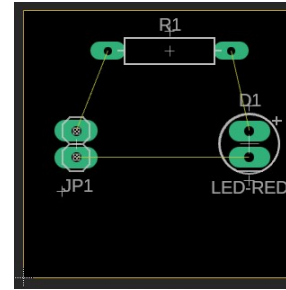
18. Ensure the **SELECT** tool is selected and move each part into the board outline area. When picking up parts, aim to click the yellow + in their centre. Rotate them while they are selected by right-clicking. Move the parts into place as shown in the following diagrams.



**Figure 46** - Move the LED and rotate it into place.



**Figure 47** - Move the Resistor into place.



**Figure 48** - Move the Connector into place.

## Traces

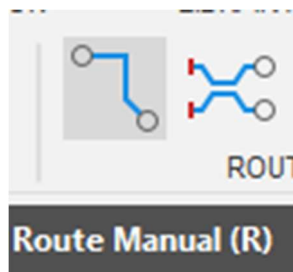
Note: The “unrouted” yellow wires connecting the parts together indicate where the traces will go.

**19.** Select the Bottom layer as the active layer. Traces made on this layer will appear in blue.



**Figure 49** - Select the Bottom layer.

**20.** In menu **DESIGN**, click on the **Route Manual** button from the **Route Controls** section.



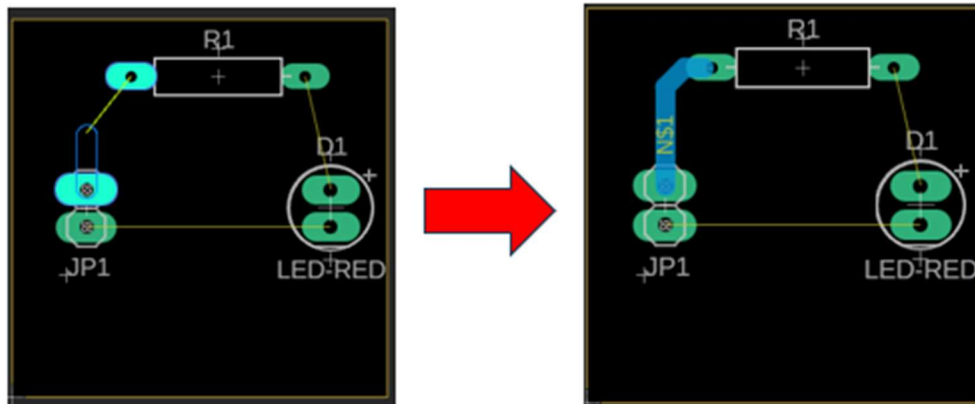
**Figure 50** - Select the Route Manual button.

**21.** Set the trace width to **50** mils. This will control how wide the traces will be in the layout.



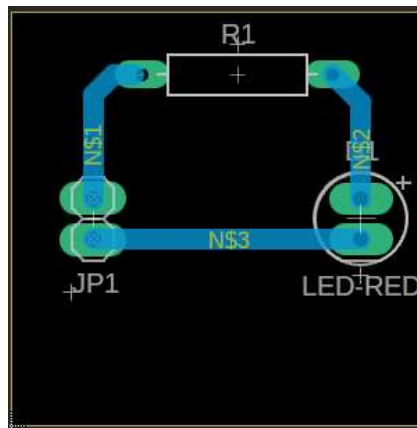
**Figure 51** - Set trace width to 50 mils.

22. Click on the top pin of JP1 to start routing the trace. Complete the trace all the way to R1.



*Figure 52 - Complete the first trace between JP1 and R1, labelled N\$1.*

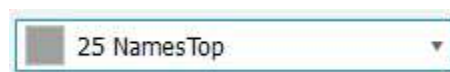
23. Continue routing all traces so that there are zero remaining unrouted wires.



*Figure 53 - All components are connected.*

### Silkscreen Text

24. To add text to the silkscreen layer, change the layer to **NamesTop**.



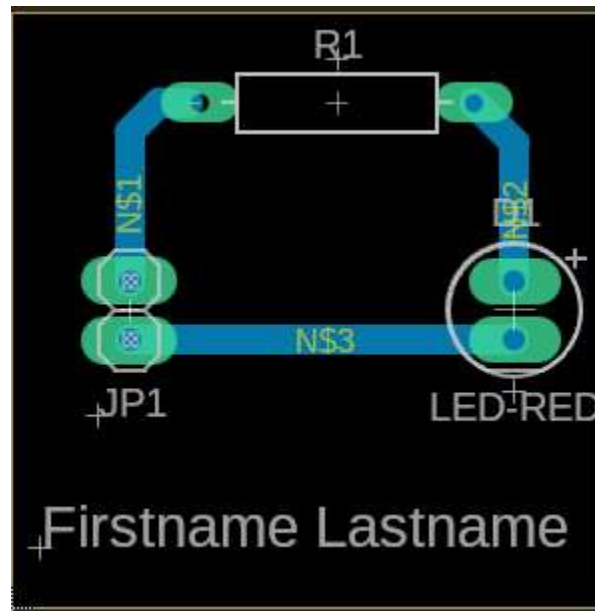
*Figure 54 - Change the layer to **NamesTop**.*

25. Click on menu **DOCUMENT** then the drop down at **DRAW** section. Select **Text** tool.



*Figure 55 - Selecting Text tool.*

26. Write your name in the text then place it in the PCB.



*Figure 56 - Finished board with all parts and text in place.*

Call your instructor for a signature: \_\_\_\_\_ ( 1 2 3 4 Marks)

27. Run the DRC process by go to menu **RULES**. Then click **Check Design Rules**.



*Figure 57 - DRC result of the current design.*

The DRC Errors window will pop up and highlight any errors or warnings, such as unconnected pins, accidental connections between power and ground, missing values, and others.

Review all errors and any warnings and fix them if necessary.

If no correction is needed on the error, click on “Approve” to remove it from the list.

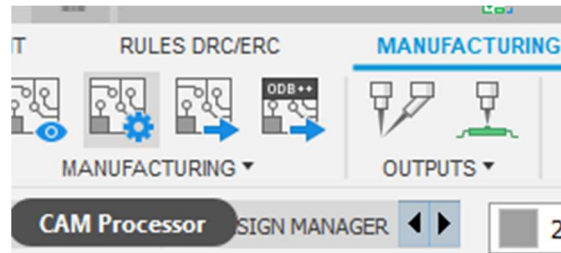
When the schematic is complete and error-free, we can proceed to the Gerber Generation.

Call your instructor for a signature: \_\_\_\_\_ ( 1 2 Marks)

## Part 4: Gerber Generation & File Submission 3 Marks

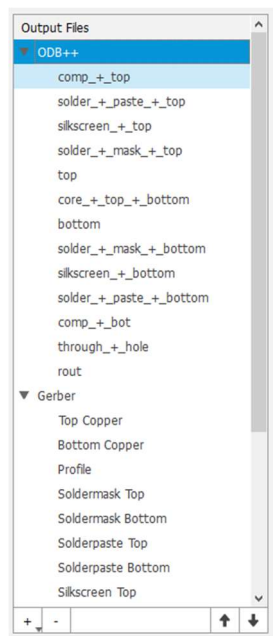
The final stage of PCB design is to create a set of manufacturing files that the machine can use to physically create the board. Each layer of the PCB design will be exported into its own Gerber file. This task covers the steps to export all Gerber files.

1. On the menu bar, click **MANUFACTURING > CAM Processor**.



*Figure 58 - Opening CAM Processor.*

2. A list of possible output files shown as below.



*Figure 59 - Possible output CAM files.*

3. A very minimum, you must have the following Gerbers to manufacture a board:

- Board Outline Layer
- Drill Files (Plated and non-plated)
- Bottom-side Copper Layer
- Top-side Silkscreen Layer

eg. If you have a double-sided board, you will also need to include the Top-side Copper Layer.

- Right click on **ODB++** directory and select **Delete all**.

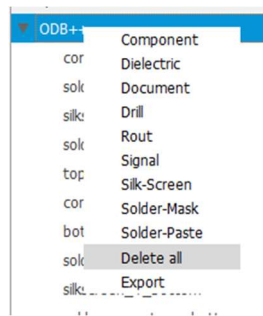


Figure 60 - Deleting ODB++ directory.

- Delete the **Assembly** directory.
- Click on the **Gerber** directory. Then change the **Gerber prefix** to **CAMOutputs**. Uncheck the **Include job file in the gerber export**.

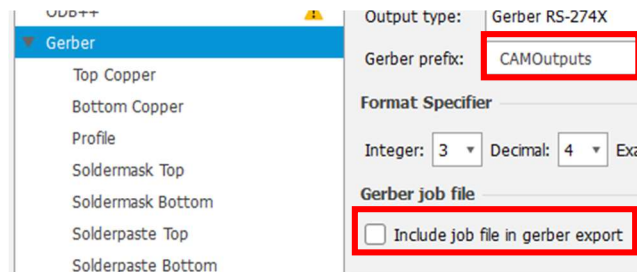


Figure 61 - Change the location for Gerber outputs.

- Under the **Gerber** directory, except for **Bottom Copper**, **Profile** and **Silkscreen Top**, delete all other files.

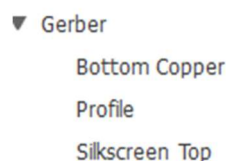


Figure 62 - Necessary Gerber outputs.

- Click on the **Drill** directory and change the **Drill prefix** to **CAMOutputs**.

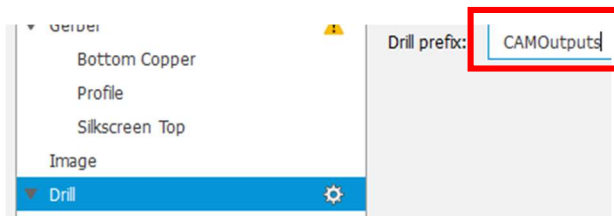
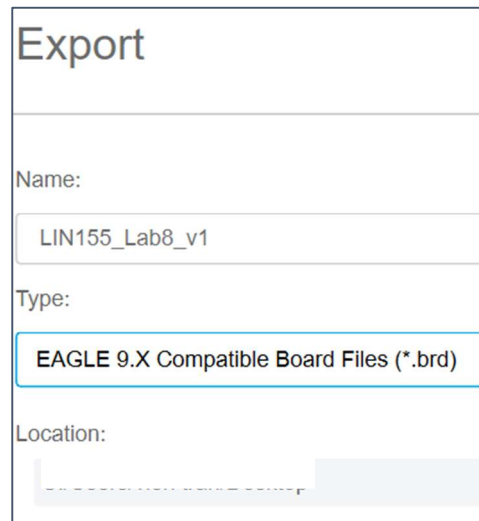


Figure 63 - Change location for Drill files.

- Click **Process Job**. Select a location and click save.

10. Export the board layout by selecting **Files > Export**.



Export

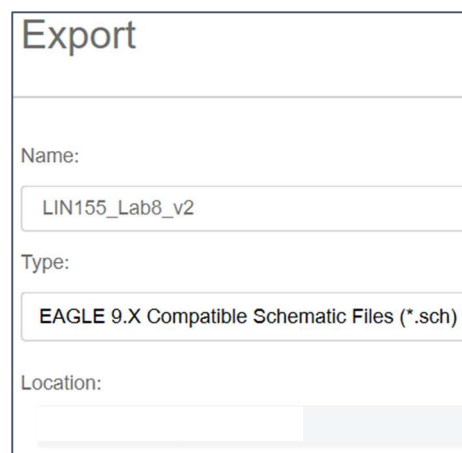
Name:  
LIN155\_Lab8\_v1

Type:  
EAGLE 9.X Compatible Board Files (\*.brd)

Location:

**Figure 64** - Exporting the board layout.

11. Switch back to the schematic then export it by going to menu **Files > Export**.



Export

Name:  
LIN155\_Lab8\_v2

Type:  
EAGLE 9.X Compatible Schematic Files (\*.sch)

Location:

**Figure 65** - Exporting the schematic.

Name: LIN155\_Lab8

Type: EAGLE 9.x Compatible Schematic Files (\*.sch)

Locations: your storage drives.

12. Using a program like File Explorer, navigate to your Gerbers folder. Put all Gerber files, plus the original \*.SCH and \*.BRD files, into a **7zip archive**. Name the file in the following format:

*LIN155NxxL\_LAB8\_LASTNAME\_FIRSTNAME.7z*

Your first and last name and lab section should be part of the 7zip archive name.

**13.** Upload the .7z file containing the **schematic** and **PCB** files, and **all Gerbers**, to Blackboard as per your professor's instructions.

Your instructor can check your 7zip archive on your screen to confirm you have included all the files, but no signature is required for this section.

**No signature. You must upload your .7z file to get full marks for the lab. ( 1 2 3 Marks)**

Total Marks:      **/15**      Comments: \_\_\_\_\_  
\_\_\_\_\_

### **Lab 8 Checklist:**

At the conclusion of this week's lab, you should be comfortable explaining the following items:

- Explain how the FUSION applications work together to produce PCB designs.
- How do the schematic and board editors work, and how are they different from each other.
- Justify your selection of components from the parts library based on their size and function.
- Understand the software rules for inserting, rotating, and modifying components.
- Explain the importance, and differences between, the DRC and ERC.
- Explain the purpose of the board outline and how to create one.
- Understand how the clearances and design rules affect the outcome of the PCB design.
- Explain how to create the Gerber files for manufacturing, and what data they each contain.
- Practice sending files to a 7zip archive, which is the file format our PCB Lab accepts by email.